

Noise Considerations for Translinear Filters

Jan Mulder, Michiel H. L. Kouwenhoven, *Member, IEEE*, Wouter A. Serdijn,
Albert C. van der Woerd, and Arthur H. M. van Roermund

(Invited Paper)

Abstract—Translinear filters exhibit unconventional noise characteristics, due to the internal nonlinear behavior. In this paper, the maximal signal-to-noise-ratio and the dynamic range properties of translinear filters are discussed, and the relation between these two specifications and the inherent instantaneous companding is described.

Index Terms— Companding, continuous-time filters, noise, translinear.

I. INTRODUCTION

THE dynamic translinear (DTL) principle, or “log-domain” principle, constitutes a promising new implementation technique for linear filters [1]–[6]. Using the DTL principle, which states that a linear time derivative of a collector current can be realized through a product of a capacitance current and one or more collector currents [6], a wide variety of linear and nonlinear differential equations (DE’s) can be implemented.

The dynamic range (DR) and the maximal signal-to-noise ratio (SNR), defined in Section II, are important specifications for linear filters. The noise behavior of TL filters is, however, different from conventional filter implementation techniques, due to the explicit dependence on the exponential voltage-to-current (V – I) transfer function of the bipolar transistor.

This paper discusses the noise properties of TL filters. In Section III, the DR specifications of opamp-MOSFET- C , $g_m C$, and TL filters are compared. The companding nature of TL filters, which is related to both the DR and the maximal SNR is discussed in Section IV. Finally, the maximal SNR of TL filters is treated in Section V.

II. DEFINITIONS OF DR AND (MAXIMAL) SNR

In the literature, several definitions for the DR and the maximal SNR are commonly used. Therefore, to clarify the discussion, this section explicitly defines these quantities.

By definition:

- the (maximal) SNR equals the (maximal) ratio of the signal power to the noise power *at the same time*;
- the DR equals the ratio of the maximal signal power to the minimum acceptable signal power; the latter is usually taken as equal to the noise power in the absence of any signals, and this convention will be adopted in this paper.

Manuscript received December 20, 1996; revised August 15, 1997. This paper was recommended by Guest Editors A. Payne and C. Toumazou.

The authors are with the Electronics Research Laboratory/DIMES, Delft University of Technology, 2628 CD Delft, The Netherlands.

Publisher Item Identifier S 1057-7130(98)06714-7.

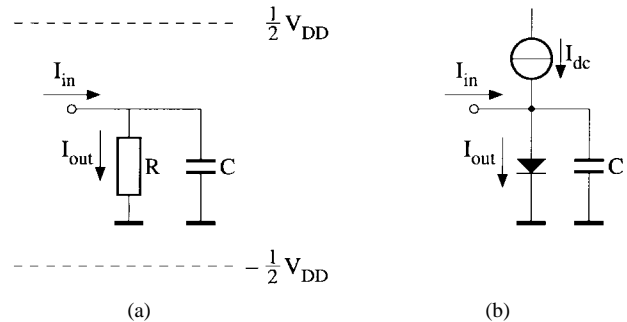


Fig. 1. Comparison of the dynamic range properties of (a) RC and (b) diode- C filter sections.

In conventional filters, the DR and the maximal SNR are equal, since the noise floor is constant. Hence, the maximal SNR is obtained for the maximum value of the signal power, which is determined by an application-specific specification of the distortion level, e.g., $<1\%$ total harmonic distortion (THD).

As pointed out in [7], a single noise figure cannot adequately describe the noise behavior of TL filters, or companding filters in general. Due to signal \times noise intermodulation, the maximal SNR can be much smaller than the DR.

III. DYNAMIC RANGE

Static translinear (STL) circuits do not have a good reputation with respect to noise. Since DTL circuits are in a way very similar to STL circuits [6], it is likely that they also inherit the noise characteristics. Therefore, it is interesting to compare the DR properties of $g_m C$ and opamp-MOSFET- C filters, the most popular implementation techniques to date [8]–[10], with the DR specifications of TL filters. Note that a comparison is made with respect to the DR and not to the maximal SNR, thus, due to the definition of the DR, excluding the signal \times noise intermodulation. This intermodulation is the topic of Section V.

The comparisons are made under the practically relevant restriction of a low supply voltage. Though DTL circuits find wider application than only linear DE’s, in this section, the comparisons are restricted to linear filters.

To obtain an indication of the DR properties of TL, $g_m C$ and opamp-MOSFET- C filters, we compare the RC and the diode- C subcircuits shown in Fig. 1. The DR of a complete filter is strongly related to the DR of these elementary building blocks. For both filter sections, the low-pass transfer function $I_{in} \rightarrow I_{out}$ is considered. Under certain presumptions, to be discussed later, the circuit shown in Fig. 1(a) is represen-

tative for opamp-MOSFET- C filters. The circuit shown in Fig. 1(b) represents both TL filters and “bipolar $g_m C$ ” filters, for which the transconductances comprise bipolar transistors only. The DR properties of the two filter sections are first compared based on a simplified approach. The influence of low-voltage implementation issues, tunability, low-power operation, high-frequency performance, and class AB operation is discussed next.

For the RC filter section, the current signal swing is limited, due to the supply voltages, to $V_{DD}/(2R)$. Assuming class A operation, the signal swing in the diode- C filter is limited by the dc bias current I_{dc} .

The only noise source in the RC filter is due to the resistor R . The double-sided noise current power spectral density is given by $2kT/R$. The noise bandwidth of the filter equals $1/(2RC)$. Hence, the equivalent noise power is found to be $kT/(CR^2)$.

In the diode- C circuit, the power spectral density of the shot noise in the bias point equals qI_{dc} . In comparing the two filters, both the capacitance value and the bandwidth of the filter are assumed to be equal. As a result, the relation between R and I_{dc} is given by $RI_{dc} = U_T$, where U_T is the thermal voltage. Hence, the noise bandwidth of the TL filter equals $I_{dc}/(2CU_T)$, and the equivalent noise power is found to be $qI_{dc}^2/(2CU_T)$.

Dividing the maximal signal swing by the total amount of noise, we find the dynamic ranges DR_{RC} and $DR_{g_m C}$ for the RC and the diode- C filter sections, respectively. This yields

$$DR_{RC} = \frac{CV_{DD}^2}{4kT} \quad (1)$$

$$DR_{g_m C} = \frac{2CU_T}{q} \quad (2)$$

Equations (1) and (2) represent upper limits of the DR. In practice, these values have to be divided by the square of the crest factor of the specific signal being processed.

To compare the dynamic range properties, we divide DR_{RC} by $DR_{g_m C}$:

$$\frac{DR_{RC}}{DR_{g_m C}} = \frac{V_{DD}^2}{8U_T^2} \quad (3)$$

Obviously, in principle, application of filters based on linear resistors yields a much better DR. For example, even for a low supply voltage of 1 V, and $U_T = 26$ mV, DR_{RC} and $DR_{g_m C}$ differ by a factor of 185, or equivalently, 22.7 dB. Since the minimal power consumption of a filter is fundamentally related to the desired DR [9], the voltage swings should be preferably rail-to-rail [9], [11]. This is realized in the RC section, but not in the diode- C subcircuit, where the voltage swing is only U_T , corresponding to a current swing of I_{dc} . This explains the large difference between DR_{RC} and $DR_{g_m C}$.

However, the conclusion drawn from (3) is not absolute. Many adventitious factors that affect the DR are not incorporated in (1) and (2). Their influence will be discussed next.

A. Opamp-MOSFET- C Filters

The opamp-MOSFET- C technique is the only method to realize filters with rail-to-rail signal swings and low noise

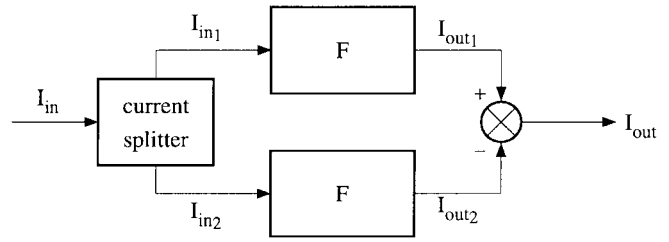


Fig. 2. Setup for class AB operation.

levels [9]. In opamp-MOSFET- C filters, large voltage swings are possible, due to the fact that the quadratic behavior of the MOS transistor in strong inversion is not very nonlinear. Thus, the subcircuit shown in Fig. 1(a) can be used to represent this class of filters. Based on the simple MOS square law equation, it is even possible, in theory, to obtain a perfectly linear transconductance, which extends the voltage swings [12]. Consequently, these filters can be made to approach the fundamental limit regarding the minimal power consumption for a certain specified DR [9].

Unfortunately, at low supply voltages, opamp-MOSFET- C filters become difficult to implement [8]–[10], resulting in a lower DR than indicated by (1). Due to the requirement for strong inversion operation, very low voltage operation becomes only possible by using an on-chip charge pump to drive the gate voltages high. In addition, the tuning range of these filters is quite limited; it is only just enough to cope with process tolerances [8].

B. MOS $g_m C$ Filters

The class of $g_m C$ filters can be divided into the categories of “MOS $g_m C$ ” and “bipolar $g_m C$,” based on MOS and bipolar transconductors, respectively. With respect to opamp-MOSFET- C filters, the excess noise of the transconductors in MOS $g_m C$ filters results in a factor 2 to 3 lower DR. Since most of the other characteristics of MOS $g_m C$ filters are very similar to opamp-MOSFET- C filters, MOS $g_m C$ filters will not be discussed here.

C. Bipolar $g_m C$ Filters

Since the bipolar transistor is an exponential device, the circuit shown in Fig. 1(b) can be used to represent bipolar $g_m C$ filters. Equation (3) shows that the DR of bipolar $g_m C$ filters is generally worse in comparison with opamp-MOSFET- C filters, since the voltage swings are limited to U_T . In practice, however, the voltage swings are even smaller due to the strongly nonlinear nature of the bipolar transistor. Therefore, most often the differential pair is used instead of a single transistor to eliminate even order distortion. Nevertheless, the voltage swings remain limited to only $0.5U_T$ for a THD of 1% [10]. The application of emitter degeneration resistors is often not allowed as this severely reduces the tuning range.

Transconductance linearization techniques using linear combinations of collector currents are not as effective for bipolar as for MOS transconductors and cannot increase the maximal voltage swings above 100 mV_{pp} [13]. Whereas for MOS transconductors exact linearization is possible, as the square

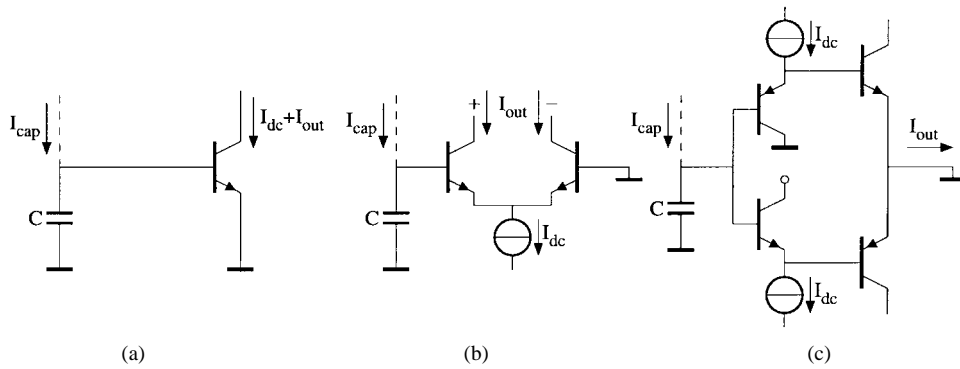


Fig. 3. Generic output structures of (a) log-domain filters, (b) tanh filters, and (c) sinh filters.

law is a polynomial, exact linearization of a bipolar transconductance is fundamentally impossible, since the exponential function is transcendental.

Although the small voltage swings in bipolar g_m - C filters have a negative influence on the DR properties, on the other hand, it makes them very suitable for operation at low supply voltages [8], [10], [13]. The DR is, to first order, independent of the supply voltage. Further, bipolar g_m - C filters exhibit very wide tuning ranges, and potential for high-frequency and low-power operation.

D. Translinear Filters

Since the diode- C circuit shown in Fig. 1(b) represents both bipolar g_m - C and TL filters, these two types of filters have many characteristics in common, e.g., excellent tunability and potential for low-voltage and low-power operation.

A major difference is formed by the possible signal swings. Owing to the application of the DTL principle, in theory, TL filters offer a perfectly linear current-mode transfer function. Hence, the maximal signal swings in TL filters are larger than in bipolar g_m - C filters. A DR comparison between a bipolar g_m - C , a log-domain, and a tanh filter, reported in [3], shows that the latter two outperform the g_m - C filter by 13 and 10 dB, respectively.

A very important aspect of log-domain filters is the possibility of class AB operation, which can be used to increase the DR [3], [4]. For example, in [4], a DR of 65 dB is reported, in connection with a maximal SNR of 52.5 dB. A generic class AB setup is shown in Fig. 2. A bipolar valued signal is split up into two unipolar signals, which are subsequently processed by different signal paths. Since the two different signal paths only have to process unipolar signals, no dc bias current is needed. Hence, the noise floor is decoupled from the maximal current signal swings, in contrast with the class A setup shown in Fig. 1(b). Class AB operation is possible due to the fact that the linearization mechanism of TL filters is theoretically exact.

E. Summary

To conclude, in principle, opamp-MOSFET- C filters are the best choice for a large DR, in the area of tuneable continuous-time filters. However, in low voltage environments, or for applications where a large tuning range is required, TL filters and bipolar g_m - C are more suitable.

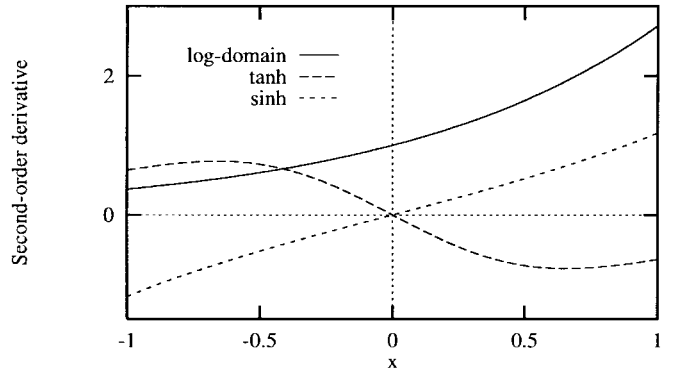


Fig. 4. The second-order derivatives of the V - I transfer functions of the circuits shown in Fig. 3.

Owing to the theoretically exact linearization mechanism, TL filters form an interesting and competitive alternative to g_m - C filters. Especially when class AB operation is applied, the DR of TL filters exceeds the DR obtainable with bipolar g_m - C filters.

IV. COMPANDING

In TL filters, the voltages are logarithmically related to the currents. Therefore, these circuits are in some way instantaneously companding. The exact nature of the companding affects the relation between the DR and the maximal SNR of a circuit. In this section, we apply the strict definition of companding, where the input compression and output expansion factors are larger than one. Only for filters that are companding according to this definition can the DR be significantly larger than the maximal SNR.

The companding properties of log-domain, tanh, and sinh filters [3] can be characterized by the V - I transfer functions of the generic output structures of these circuits, depicted in Fig. 3. The V - I “expansion” functions are given by $\exp x$, $\tanh x$, and $\sinh x$, respectively, where x represents the normalized capacitance voltage swing. Fig. 4 depicts the second-order derivatives of the V - I transfer functions. For an output stage complying to the strict definition of (instantaneous) expansion, this second-order derivative should be strictly positive for $x > 0$ and strictly negative for $x < 0$.

Fig. 4 demonstrates that the sinh transconductor is indeed companding. This fact is related to the inherent class AB

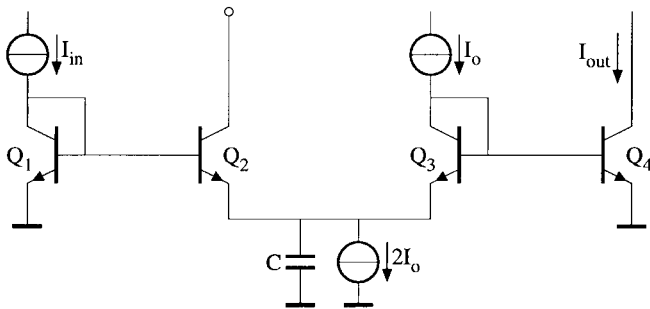


Fig. 5. A first-order translinear low-pass filter.

properties of sinh filters [3]. Large output current swings are thus facilitated without the requirement of a large dc bias current. Hence, the DR can be much larger than the maximal SNR.

The second-order derivative of the tanh function shows the opposite behavior with respect to the sinh transconductor. In other words, the differential pair *output* stage, shown in Fig. 3(b), implements a *compression* operation [5]. The signal swing is limited by the tail current. As a result, the DR and the maximal SNR will not differ much.

A similar conclusion can be reached for log-domain filters operated in class A. The second-order derivative of the function $\exp x$ is positive both for $x > 0$ and $x < 0$. Thus, these filters are not strictly companding, and hence, the maximal SNR and the DR will be approximately equal.

However, this conclusion is not valid for log-domain filters operated in class AB. Using the setup shown in Fig. 2, only positive signals have to be processed. Since for $x > 0$ the second-order derivative of the function $\exp x$ does correspond to the characteristics of an expansion stage, class AB operated log-domain filters are companding. This results in a significantly improved DR over the class A counterpart. It is interesting to note that tanh filters cannot be operated in class AB, since $\tanh x$ constitutes a compression for both positive and negative signals.

V. MAXIMAL SNR

Due to the internal nonlinear behavior, causing signal \times noise intermodulation, the noise floor in a TL filter is signal-dependent. Since class A operated TL filters, i.e., log-domain and tanh filters, are not companding, nonlinear noise effects are not very pronounced. In class AB operated filters, however, the intermodulation noise will dominate for large values of the signal power. The intermodulation noise power is proportional to the signal power, and consequently, the maximal SNR is fundamentally limited. Thus, only the DR, and not the maximal SNR, can be improved through companding [10].

The limited value of the maximal SNR can be explained intuitively from the first-order TL filter depicted in Fig. 5. For this circuit, class AB operation can be established using the setup shown in Fig. 2.

Fig. 6 shows that this filter can be redrawn as a cascade of two ports. It is obvious that the SNR of a chain of two ports is limited by the element(s) having the lowest SNR. A translation of this fact to the circuit shown in Fig. 6 indicates

that the maximal SNR of the class AB TL filter is limited by the transistors with the lowest SNR value. In this context, the “SNR of a bipolar transistor” equals the ratio of the mean square value of the collector current I_C , to the power of the collector shot noise current i_C .¹ Since the mean square value P_{I_C} of the collector current is proportional to I_C^2 , and the noise power P_{i_C} , over a certain noise bandwidth B_n , is proportional to I_C , the SNR of a bipolar transistor is proportional to I_C .

In Fig. 5, for increasing input signal swings and class AB operation, the average collector currents of Q_1 and Q_4 increase accordingly. Hence, the SNR of Q_1 and Q_4 increases. The average collector currents of Q_2 and Q_3 , however, remain equal to I_o . Thus, for large signals, the SNR of these two transistors dictates the maximal SNR value of the complete filter. For Q_3 , P_{I_C} equals I_o^2 . The (double-sided) power spectral density of i_C is equal to qI_o , and the noise bandwidth B_n of the filter equals $I_o/(2CU_T)$. Hence, P_{i_C} becomes $qI_o^2/(2CU_T)$, and the SNR of Q_3 is found to be $2CU_T/q$. Since Q_2 and Q_3 have the same SNR, the overall SNR is reduced by a factor two. This yields

$$\text{SNR}_{\max} = \frac{CU_T}{q}. \quad (4)$$

The exact value of the SNR of the filter for low and intermediate signal levels can only be obtained from detailed calculations, using the method outlined in [14]. In Fig. 7, the solid line shows the SNR of the class AB filter depicted in Fig. 5, using the setup shown in Fig. 2. It is assumed that the noise contribution of the dc current sources is negligible. A sinusoidal input signal is applied, i.e., $I_{\text{in}} = m I_{\text{dc}} \sin \omega t$, where I_{dc} is the quiescent current level of the (geometric mean) class AB splitter, m is the modulation index, and ω is an in-band frequency. The parameter values used in this plot are $I_{\text{dc}} = I_o = 1 \mu\text{A}$, $C = 10 \text{ pF}$, and $U_T = 26 \text{ mV}$. For low values of m , the SNR increases by 20 dB per decade. For high values of m , the SNR saturates to a value of 62.1 dB, as predicted by (4).

A. Seevinck's Class AB Integrator

Although exhibiting an externally linear transfer function, the class AB integrator proposed by Seevinck in [2], and shown in Fig. 8, is in a way a nonlinear DTL circuit as it implements two *nonlinear* DE's. The two TL loops of this integrator are described by

$$CU_T \dot{I}_{\text{out}1,2} + I_{\text{out}1} I_{\text{out}2} = I_o I_{\text{in}1,2}. \quad (5)$$

A linear integrator is obtained through subtraction of these equations. The overall input and output current equal $I_{\text{in}} = I_{\text{in}1} - I_{\text{in}2}$ and $I_{\text{out}} = I_{\text{out}1} - I_{\text{out}2}$, respectively.

Fig. 7 shows that the noise performance of both class AB filters is almost identical. The SNR of both filters converges to the same maximal value given by (4). However, for low values of the modulation index, Seevinck's circuit performs slightly better, about 0.6 dB, due to a lower noise floor in the quiescent point. This noise floor can be decreased further, up to 1 dB, by increasing the term proportional to $I_{\text{out}1} I_{\text{out}2}$ in (5).

¹The influence of the base resistance noise is assumed to be negligible.

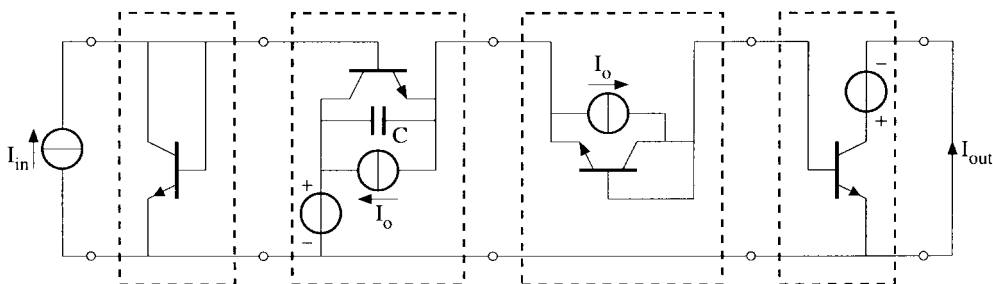


Fig. 6. A translinear filter consisting of a cascade of two ports.

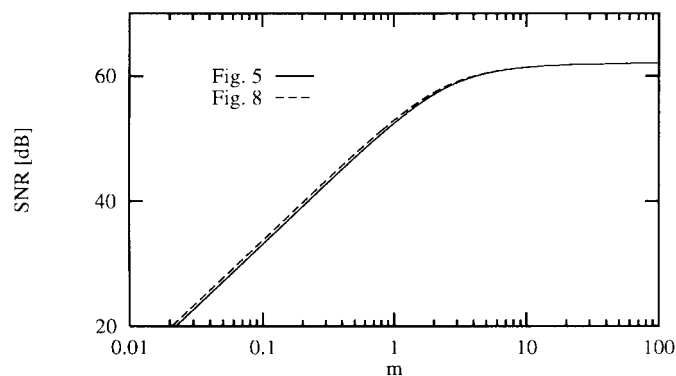


Fig. 7. SNR of a class AB translinear filter.

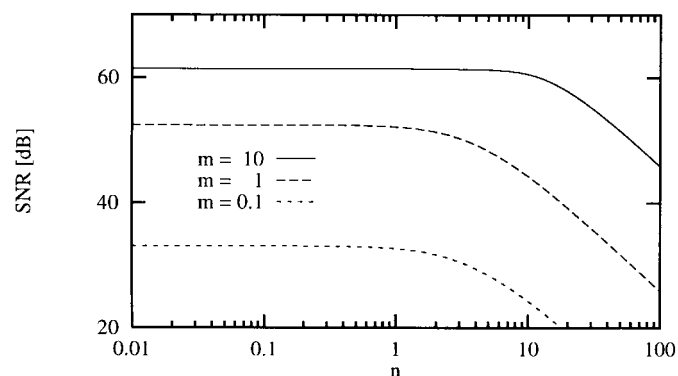


Fig. 9. SNR in the presence of an out-of-band signal.

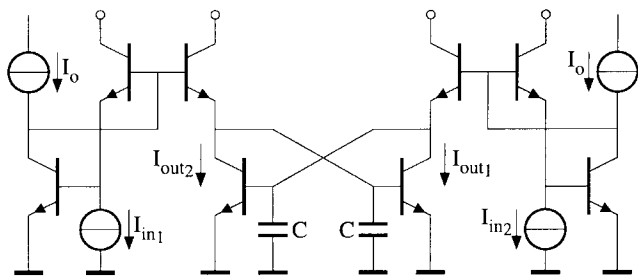


Fig. 8. Seevinck's class AB translinear integrator [2].

B. Noise Due to Out-of-Band Signals

An interesting situation is the coexistence of a large signal and a small signal in a companding TL filter. Suppose the small signal is the desired output signal and the large signal is outside the pass band. Now, in conventional filters, the large signal will limit the maximal SNR at the output, as it occupies a large part of the available DR of the filter. Naturally, the same effect applies to companding filters. However, in these filters, the SNR at the output will be further decreased, as the large out-of-band signal will increase the internal noise level [7]. This effect makes companding filters less suitable, e.g., for intermediate-frequency filtering [9], unless some form of linear prefiltering is used [7].

Fig. 9 displays the resulting SNR for an in-band signal as a function of the amplitude of an out-of-band signal, for the class AB filter shown in Fig. 5. The applied input signal equals $I_{dc} (m \sin \omega_1 t + n \sin \omega_2 t)$, where ω_1 and ω_2 are the in-band and out-of-band frequencies, respectively. The modulation index m of the in-band signal equals $[0.1, 1, 10]$, and n denotes the modulation index of the out-of-band signal.

The figure demonstrates the expected behavior. Clearly, the effect is more pronounced for in-band signals with a small amplitude, i.e., a low value of m .

VI. CONCLUSIONS

A comparison of the DR properties of different filter implementation techniques shows that class AB operated TL filters constitute an interesting and competitive alternative for low-voltage or low-power applications. The maximal SNR of a TL filter, however, is fundamentally limited. This is due to signal \times noise intermodulation, which causes the noise level to increase when large signals (even when out-of-band signals) are being processed.

REFERENCES

- [1] R. W. Adams, "Filtering in the log domain," 63rd Convention A.E.S., LA, preprint 1470, May 1979.
- [2] E. Seevinck, "Companding current-mode integrator: A new circuit principle for continuous-time monolithic filters," *Electron. Lett.*, vol. 26, no. 24, pp. 2046–2047, Nov. 1990.
- [3] D. R. Frey, "Exponential state space filters: A generic current mode design strategy," *IEEE Trans. Circuits Syst. I*, vol. 43, pp. 34–42, Jan. 1996.
- [4] M. Punzenberger and C. Enz, "A 1.2 V BiCMOS class AB log-domain filter," in *Proc. ISSCC*, 1997, pp. 56–57.
- [5] J. Mahanattakul and C. Toumazou, "Instantaneous companding and expressing: A dual approach to linear integrator synthesis," *Electron. Lett.*, vol. 33, no. 1, pp. 4–5, Jan. 1997.
- [6] J. Mulder, A. C. van der Woerd, W. A. Serdijn, and A. H. M. van Roermund, "General current-mode analysis method for translinear filters," *IEEE Trans. Circuits Syst. I*, vol. 44, pp. 193–197, Mar. 1997.
- [7] Y. Tsvividis, "Externally linear, time-invariant systems and their application to companding signal processors," *IEEE Trans. Circuits Syst. II*, vol. 44, pp. 65–85, Feb. 1997.
- [8] Y. P. Tsvividis, "Integrated continuous-time filter design—An overview," *IEEE J. Solid-State Circuits*, vol. 29, pp. 166–176, Mar. 1994.

- [9] G. Groenewold, B. Monna, and B. Nauta, "Micro-power analog-filter design," in *Analog Circuit Design—Low-Power Low-Voltage, Integrated Filters and Smart Power*, R. J. van de Plassche, W. M. C. Sansen, and J. H. Huijsing, Eds. Dordrecht, The Netherlands: Kluwer, 1995, pp. 73–88.
- [10] R. Castello, F. Montecchi, F. Rezzi, and A. Baschiroto, "Low-voltage analog filters," *IEEE Trans. Circuits Syst. I*, vol. 42, pp. 827–840, Nov. 1995.
- [11] E. A. Vittoz, "Low-power low-voltage limitations and prospects in analog design," in *Analog Circuit Design—Low-Power Low-Voltage, Integrated Filters and Smart Power*, R. J. van de Plassche, W. M. C. Sansen, and J. H. Huijsing, Eds. Dordrecht, The Netherlands: Kluwer, 1995, pp. 3–15.
- [12] Y. Tsvividis, M. Banu, and J. Khoury, "Continuous-time MOSFET-C filters in VLSI," *IEEE Trans. Circuits Syst.*, vol. 33, pp. 125–140, Feb. 1986.
- [13] H. Tanimoto, M. Koyama, and Y. Yoshida, "Realization of a 1-V active filter using a linearization technique employing plurality of emitter-coupled pairs," *IEEE J. Solid-State Circuits*, vol. 26, pp. 937–945, July 1991.
- [14] J. Mulder, M. H. L. Kouwenhoven, and A. H. M. van Roermund, "Signal \times noise intermodulation in translinear filters," *Electron. Lett.*, vol. 33, no. 14, pp. 1205–1207, July 1997.



Jan Mulder was born in Medemblik, The Netherlands, on July 7, 1971. He received the M.Sc. degree in electrical engineering from the Delft University of Technology, Delft, The Netherlands, in 1994. He is currently working toward the Ph.D. degree on static and dynamic-translinear analog integrated circuits at the Electronics Research Laboratory, Delft University of Technology.



Michiel H. L. Kouwenhoven (S'94–M'97) was born in Delft, The Netherlands, on July 8, 1971. He received the M.Sc. degree in electrical engineering and the Ph.D. degree (both *cum laude*) from the Delft University of Technology, Delft, The Netherlands, in 1993 and 1998.

Since 1997, he has been an Assistant Professor at the Electronics Research Laboratory, Delft University of Technology, where he is engaged in courses on structured electronic design, the multidisciplinary Ubiquitous Communications (UbiCom) research program, and a research project on nonlinear electronics. His main research interests include noise in nonlinear circuits and systems and the development of design methodologies for wireless communication receivers and demodulators.

Dr. Kouwenhoven received the 1997 Veder award from the Dutch Foundation for Radio Science for his doctoral work on FM demodulators.



Wouter A. Serdijn was born in Zoetermeer, The Netherlands, in 1966. He started his course at the Faculty of Electrical Engineering at the Delft University of Technology in 1984, and received the "Ingenieurs" (M.Sc.) degree in 1989 and the Ph.D. degree in 1994.

His research interests include low-voltage, ultralow-power, RF, and dynamic-translinear analog integrated circuits along with circuits for wireless communications, hearing instruments, and pacemakers. Since 1997, he has been a project leader in the multidisciplinary Ubiquitous Communications (UbiCom) research program of the Delft University of Technology. He is co-editor and co-author of the book *Analog IC Techniques for Low-Voltage Low-Power Electronics* (Delft, The Netherlands: Delft University, 1995) and of the book *Low-Voltage Low-Power Analog Integrated Circuits* (Boston, MA: Kluwer, 1995). He has authored and co-authored more than 40 publications. He teaches Analog Electronics for Industrial Designers, Analog IC Techniques, and Structured Electronic Design.



Albert C. van der Woerd was born in 1937 in Leiden, The Netherlands. He received the "Ingenieurs" (M.Sc.) degree in electrical engineering and the Ph.D. degree from the Delft University of Technology, Delft, in 1977 and in 1985, respectively.

From 1959 to 1966, he was engaged in research on and the development of radar and TV circuits at several industrial laboratories. In 1966 he joined the Electronics Research Laboratory of the Faculty of Electrical Engineering of the Delft University of Technology. For the first 11 years he carried out research on electronic musical instruments. For the next eight years his main research subject was carrier domain devices. More recently, he has specialized in the field of low-voltage low-power analog circuits and systems for hearing instruments.



Arthur H. M. van Roermund was born in Delft, The Netherlands, in 1951. He received the M.Sc. degree in electrical engineering from the Delft University of Technology, Delft, in 1975 and the Ph.D. degree in applied sciences from the K. U. Leuven, Belgium, in 1987.

From 1975 to 1992 he was with the Philips Research Laboratories, Eindhoven, The Netherlands. First he worked in the Consumer Electronics Group on design and integration of analog circuits and systems, especially switched-capacitor circuits. In 1987 he joined the Visual Communications Group where he has been engaged in video architectures and digital video signal processing. From 1987 to 1990 he was project leader of the Video Signal Processor project and from 1990 to 1992 of a Multiwindow Television project. Since 1992, he has been a Full Professor at the Electrical Engineering Department of the Delft University of Technology where he is heading the Electronics Research Laboratory. He is also group leader of the Electronics Group and coordinator of the Circuits and Systems Section of the Delft Institute of Micro Electronics and Submicron (DIMES) technology, which is a cooperation between research groups on micro electronics, technology, and technology-related physics.